

Systems, Networks & Concurrency 2020

Introduction to Concurrency

Why do we need/have concurrency?

- Physics, engineering, electronics, biology, ...
- ex** basically every real world system is concurrent!
- Sequential processing is suggested by most core computer architectures
 - yet almost all core processor cores have concurrent elements
 - ... and most computer systems are part of a concurrent network
- Strict sequential processing is suggested by widely used programming languages.
- ex** Sequential programming delivers some fundamental components for concurrent programming
- but we need to add a number of further crucial concepts**

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Forms of concurrency

What would a computer scientist consider concurrency?

- to be able to connect computer systems with the real world
- to be able to employ design concurrent parts of computer architectures
- to construct complex software packages operating systems, compilers, databases, ...
- to understand when sequential and when sequential or concurrent programming is required
- or to understand and when sequential or concurrent programming can be chosen freely
- to enhance the reactivity of system
- to enhance the performance of system
- to be able to design embedded systems
- ...

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Forms of concurrency

Why is concurrency?

Working definitions:

- Literally concurrent means:
 - Adj. Running together in space, as parallel lines; going on side by side, as proceedings; occurring together, as events or circumstances existing or arising together; conjoint, associated (Oxford English Dictionary)
- Technically concurrent it's usually defined negatively as:
 - If there is no observer who can identify two events as being in strict temporal sequence (i.e. one event has fully terminated before the other one started) then these two events are considered concurrent.

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Forms of concurrency

A computer scientist's view on concurrency

Terminology for physically concurrent machines architectures:

- SISD**
 - Single instruction, single data
 - Sequential processes
- MISD**
 - Multiple instruction, single data
 - Multiple instruction, single data
- SIMD**
 - Multiple instruction, multiple data
 - Vector processors

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An engineer's view on concurrency

Multiple physical, coupled, dynamical systems form the actual environment and/or task at hand

- ex** Multiple physical, coupled, dynamical systems form the actual environment and/or task at hand
- ex** Multiple, less powerful, processes are often preferred over a single high-performance cpu
 - The system design of usually strictly based on the structure of the given physical system.

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Does concurrency lead to chaos?

Concurrency often leads to the following features / issues / problems:

- non-deterministic phenomena
- non-reversible system states
- results may depend on more than just the input parameters and states at start time (timing, throughput, load, available resources, signals, ... throughout the execution)
- non-repeatable e. debugging?

Meaningful employment of concurrent systems features:

- non-determinism employed where the underlying system is non-deterministic
- non-determinism employed where the actual execution sequence is meaningless
- synchronization employed where adequate ... but only there

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Concurrent programming abstraction

What appears sequential on a higher abstraction level,

1. What appears sequential on a higher abstraction level,
is usually concurrent at a lower abstraction level:

- e.g. Concurrent operating system or hardware components,

2. What appears concurrent on a higher abstraction level,
might be sequential at a lower abstraction level:

- e.g. Multi-processing system, which are executed in a single, sequential computing node

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The concurrent programming abstraction

Concurrent program :=

Multiple sequential programs (processes or threads)
which are executed concurrently.

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The concurrent programming abstraction

Concurrent execution between concurrent parts means that we can analyze them individually as pure sequential programs (end of cause).

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No interaction between concurrent system parts means that we can analyze them individually as pure sequential programs (end of cause).

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P.S. it is generally assumed that concurrent execution means that there is one execution and (process per) sequential program even though this is usually not technically correct. It is still an often valid, conservative assumption in the context of concurrent programming.

- concurrent in the context of programming and logic:
 - "**Concurrent programming abstraction** is the study of interleaved execution sequences of the atomic instructions of sequential processes."

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Correctness vs. testing in concurrent systems:

- Slight changes in external triggers may (and usually does) result in completely different schedules (interleaving):
 - Concurrent programs which depend in any way on external influences cannot be tested without modelling and simulating those influences and the test processes.
 - Designs which are provably correct with respect to the actual timing behavior are essential.
 - PS: some timing restrictions on the scheduling of threads are essential in non-real-time systems, e.g. fairness

• holds true for all possible sequences of interaction points (interleavings)

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Correctness of concurrent non-real-time systems

Logical correctness!:

- does not depend on clock speeds / execution times / delays
- does not depend on actual interleaving of concurrent processes
- holds true for all possible sequences of interaction points (interleavings)

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Extended concepts of correctness in concurrent systems:

- Termination is often not intended or even considered a failure

Safety properties: $(P(I) \wedge \text{Processes}(I, S)) \Rightarrow \square Q(I, S)$ where $\square Q$ means that Q does always hold

- Mutual exclusion (no resource collisions)
- Absence of deadlocks (and other forms of 'silent death' and 'freeze' conditions)
- Specified responsiveness or free capabilities typical in real-time / embedded systems or server applications

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Liveness properties: $(P(I) \wedge \text{Processes}(I, S)) \Rightarrow \lozenge Q(I, S)$ where $\lozenge Q$ means that Q does eventually hold (and will then stay true) and S is the current state of the concurrent system

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Safety properties: $(P(I) \wedge \text{Processes}(I, S)) \Rightarrow \square Q(I, S)$ where $\square Q$ means that Q does always hold

Examples:

- Mutual exclusion (no resource collisions)
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The concurrent programming abstraction

1 CPU for all control-flows

Standard concepts of correctness:

- Partial correctness:
- Total correctness:
 - Program terminates: $(P(I) \wedge \text{terminates}(\text{Program}(I, O))) \Rightarrow Q(I, O)$
 - Process terminates: $P(I) \Rightarrow (\text{terminates}(\text{program}(I, O)) \wedge Q(I, O))$ where I , O are input and output sets. A property of the input set, and Q is a property of the output set. and Q is a property of the input set, and Q is a property of the output set

• do those concepts apply to and are sufficient for concurrent systems?

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1 CPU per control-flow

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Processes

Introduction to processes and threads

Process Control Blocks (PCBs)

Processes = Address space + Control-flows

Kernel has full knowledge about all processes as well as their states, requirements and currently held resources.

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Threads

Threads (individual control-flows) can be handled as:

- Inside the OS:
 - Kernel-scheduling: Thread can easily be connected to external events (IO).
 - Outside the OS:
 - User-level scheduling:
 - Threads may need to go through their parent-processes to access IO.
 - e.g. kernel-level control-flows, which are handled as processes with some restrictions, state or condition awaiting access or change in state

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Symmetric Multiprocessing (SMP)

All CPU share the same physical address space (and access to resources).

Any process/thread can be executed on any available CPU.

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Time-line or Sequence?

Consider time (durations) explicitly:

- Real-time systems join the appropriate courses

Consider the sequence of interaction points only:

- Non-real-time systems stay in your seat

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Atomic operations:

Correctness proofs/designs in concurrent systems rely on the assumptions of 'Atomic operations' (detailed discussion later):

- Complex and powerful atomic operations ease the correctness proofs, but may limit flexibility in the design
- Simple atomic operations are theoretically sufficient, but may lead to complex systems which correctness cannot be proven in practice.

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Atomic operations:

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- Atomic operations' detailed discussion later:

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1 CPU per control-flow

Specific configurations only; e.g.:

- Distributed job-schedulers.
- Parallel processes
- control systems:
- 1 CPU per task, connected via a bus-system.
- Processor management scheduling required.
- Shared memory access needed to be coordinated.

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No interaction between concurrent system parts means that we can analyze them individually as pure sequential programs (end of course).

Interaction occurs in form of:

- Contention** (implicit interaction units)
 - Multiple concurrent execution units compete for one shared resource.
 - Communication (explicit interaction):
 - Explicit passing of information and/or explicit synchronization.

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